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JR.	PATENT DATE	

PATENT NUMBER

APPLICATION NO. 09/666156	CONT/PRIOR F	CLASS 257	SUBCLASS 335.	2811	EXAMINER 1407	Loke ms
Haruko In Yuichi Ki			•	,		,

High-voltage MOS transistor and method for fabricating the same $\frac{1}{4}$

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TERMINAL		DRAWINGS		CLAIMS ALLOWED			
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.		
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